

<b>L Number</b>	<b>Hits</b>	<b>Search Text</b>	<b>DB</b>	<b>Time stamp</b>
-	382	375/240.25.ccls.	USPAT	2003/09/24 15:16
-	6	375/240.25.ccls. and decoder and CPU same (motion adj1 compensation)	USPAT	2003/09/24 15:20
-	60	decoder and CPU same (motion adj1 compensation)	USPAT	2003/09/24 15:56
-	57	CPU with (motion adj1 compensation)	USPAT	2003/09/24 16:19
-	4	Programmable with processor with (motion adj1 compensation)	USPAT	2003/09/24 16:20
-	10	Programmable with (motion adj1 compensation)	USPAT	2003/09/25 09:15
-	46	Program with (motion adj1 compensation)	USPAT	2003/09/25 09:16
-	13110	keith.in.	USPAT	2003/09/25 10:16
-	296	keith.in. and intel	USPAT	2003/09/25 10:26
-	18	(keith.in. and intel) and mpeg	USPAT	2003/09/25 10:17
-	30959	intel	USPAT	2003/09/25 10:20
-	8	intel and mpeg with coprocessor	USPAT	2003/09/25 10:23
-	15	kieth.in.	USPAT	2003/09/25 10:27
-	0	britton.xa. and keith	USPAT	2003/09/25 10:28
-	0	britton.xa. and kieth	USPAT	2003/09/25 10:28
-	0	britton.xa. and kiethe	USPAT	2003/09/25 10:28
-	111	britton.xa.	USPAT	2003/09/25 10:43
-	91	michael adj1 keith.in.	USPAT	2003/09/25 10:44
-	0	(michael adj1 keith.in.) and intel.asn.	USPAT	2003/09/25 10:44
-	2	(michael adj1 keith.in.) and intel	USPAT	2003/09/25 10:44
-	166	michael near2 keith.in.	USPAT	2003/09/25 10:44
-	52	(michael near2 keith.in.) and intel.asn.	USPAT	2003/09/25 10:45
-	0	((michael near2 keith.in.) and intel.asn.) and coprocessor	USPAT	2003/09/25 10:45
-	50	((michael near2 keith.in.) and intel.asn.) and \$3processor	USPAT	2003/09/25 10:50
-	217	Motion adj1 compensation with \$2processor	USPAT	2003/09/25 10:51

-	20	Motion adj1 compensation with \$2processor same (CPU or host)	USPAT	2003/09/25 10:51
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